

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
1 December 2005 (01.12.2005)

PCT

(10) International Publication Number
WO 2005/114294 A1

(51) International Patent Classification⁷: **G02B 26/00**

(21) International Application Number:
PCT/US2005/013464

(22) International Filing Date: 20 April 2005 (20.04.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/844,819 12 May 2004 (12.05.2004) US

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(81) Designated States (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW.

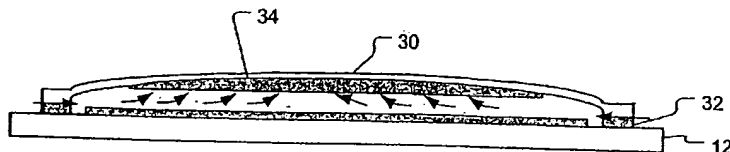
(84) Designated States (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: PACKAGING FOR AN INTERFEROMETRIC MODULATOR



(57) Abstract: A package is made of a transparent substrate (12) having an interferometric modulator (10) and a back plate (30). A non-hermetic seal (32) joins the back plate to the substrate to form a package, and a desiccant (34) resides inside the package. A method of packaging an interferometric modulator includes providing a transparent substrate and manufacturing an interferometric modulator array on a backside of the substrate. A back plate is provided and a desiccant is applied to the back plate. The back plate is sealed to the backside of the substrate with a back seal in ambient conditions, thereby forming a package.

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PACKAGING FOR AN INTERFEROMETRIC MODULATOR

Background

Interferometric modulators are spatial light modulators typically used in display applications. These modulators use interference effects to control the intensity and the color of the light seen by a viewer. One example of such a modulator is the iMoD™. The iMoD employs a cavity having at least one movable or deflectable wall. As the wall, typically comprised at least partly of metal, moves towards a front surface of the cavity, interference occurs that affects the color of light viewed at the front surface. The front surface is typically the surface where the image seen by the viewer appears, as the iMoD is a direct-view device.

The front surface is transparent, as it is the portion of the device through which the viewer views the image. Manufacture of an interferometric modulator is somewhat similar to other types of devices manufactured on glass, such as liquid crystal devices (LCD). However, due to the unique nature of the device, packaging of the device has some other considerations.

Interferometric modulators are microelectromechanical systems (MEMS) devices. While they use a glass substrate like LCDs, they also have moving parts and must have room to move while remaining protected. Similarly, while they are MEMS devices, they are manufactured on a substrate that has different processing considerations than most MEMS devices.

There are some degrees of freedom available to interferometric modulators not available to either LCDs or many MEMS devices. Unlike LCDs, interferometric modulators do not have material injected into the package, such as the liquid crystalline material is into LCD display panels. Unlike many MEMS devices, interferometric modulators do not have parts that rub against one another, alleviating concerns about internal particle generation.

Brief Description of the Drawings

The invention may be best understood by reading the disclosure with reference to the drawings, wherein:

Figure 1a and 1b shows an example of an interferometric modulator array.

Figure 2 shows a side view of an embodiment of an interferometric modulator package.

Figure 3 shows a flow chart of an embodiment of a method to package interferometric modulators.

Figure 4 shows an embodiment of a back plate having recessed regions.

Figure 5 shows an embodiment of a back plate having recessed regions formed by a frame applied to the substrate.

Figure 6 shows an embodiment of a package having a hole for application of desiccant.

Figure 7 shows an embodiment of an interferometric modulator packaging having a non-hermetic seal.

Detailed Description of the Embodiments

Interferometric modulators, such as the iMoD™, rely upon interference effects operating on light inside the cavity to modulate the light in accordance with image data. Cross-sectional views of such a modulator 10 are shown in Figures 1a and 1b. In this embodiment, the viewing surface would be at the 'bottom' of the picture, as shown by the viewer eye 14. The modulator array is formed on a transparent substrate 12. An optical stack 16 forms a first optically active surface that may be affected by the second optically active surface, the mechanical or mirror layer 22. A dielectric layer 18 typically protects the optical stack layer. The mechanical layer 22 is supported by posts such as 24, with the location of posts forming the individual elements of the array.

When the circuitry on the substrate, not shown, is activated in a particular region under the mechanical layer, such as that portion that is suspended over cavity 20, the mechanical layer deflects towards the optical stack 16. As it deflects, the mechanical layer causes the portion of the optical stack as seen by the viewer to appear black. Therefore, by addressing the mechanical layer with image data, an image would be seen by the viewer. This particular embodiment of an interferometric modulator may be referred to as a monolithic interferometric modulator here. This embodiment and those similar to it will be referred to as a membrane interferometric modulator.

In an alternative embodiment of an interferometric modulator shown in Figure 1b, the mirror 26 that causes the pixel to appear black when deflected is separated from the support layer 22. This may be referred to as a separable modulator here. In either case, the package in which the modulator is encapsulated must account for moisture inside the package that can cloud the transparent substrate 12, as well as any outgassing that may occur from the structure of the modulator layers or package materials. It has some degrees of freedom in that there are no particulates generated by the operation of the device and the devices may be packaged in ambient conditions.

Figure 2 shows an embodiment of an interferometric modulator in a package. The package is formed between the transparent substrate 12, with the modulator 10 on the 'back side' of the substrate relative to the viewer 14, and a back plate 30. The transparent substrate and the back plate can be joined with a non-hermetic seal 32 joining the back plate to the substrate to form a package. Generally, the package will contain a desiccant such as 34 between the backside of the modulator array and the back plate. This allows for control of the moisture inside the package.

Desiccants are used in other device packages, being a fairly effective means to control moisture inside the package. Desiccants may be used for packages that do or do not have hermetic seals. Hermetic seal, as that term is used here, refers to a seal that is complete, especially with regard to air and water vapor flow. In devices with a hermetic seal, desiccants are

typically applied to control moisture resident inside the package. If the package does not have a hermetic seal, a desiccant may be used to control moisture moving into the package.

5 In the package, the transparent substrate may be any transparent substance that is capable of having thin film, MEMS devices built upon it. Examples include glass, plastic, and transparent polymers. Similarly, the interferometric modulators may be membrane modulators, or modulators of the separable type.

10 The back plate and the transparent substrate, when brought together may have recessed regions or cavities formed in between them where the modulator arrays reside. The back plate may be glass, metal, foil, polymer, plastic, and ceramic or semiconductor materials such as silicon. The seal between the transparent substrate and the back plate may be an epoxy-based adhesive, o-rings, a PIB, poly-urethane, thin film metal weld, liquid spin-on glass, solder, polymers, and plastics, among others.

15 Generally, the desiccant is used to control moisture, so any substance that can trap moisture while not interfering with the optical properties of the modulators may be used. Examples include zeolites, molecular sieves, surface adsorbents, bulk adsorbents, and chemical reactants.

20 In order to provide this type of package to interferometric modulators, cost and complexity become a concern. It is generally desirable to manufacture several arrays of modulators on one transparent substrate, apply the back plate and then split the substrate into individual devices. Some parts of the packaging process may be applied before the division into individual devices as well as after the division.

25 Figure 3 shows a method of packaging an interferometric modulator. A transparent substrate is provided at 40 and the modulator array is provided at 42. For faster processing times, as well as more efficient use of the manufacturing lines, it may be desirable to process the back plate at the same time as the transparent substrate. However, this is merely one option and is not required. The back plate is provided at 44 and the desiccant applied at 46. Application of the desiccant may be accomplished after the process of joining the substrate to the back plate, typically by a process of injection, as will be discussed further.

30 The back plate and the backside, relative to the viewer 14 of Figure 2, of the transparent substrate are sealed in ambient conditions to form the package. Ambient conditions as used here mean any combination of conditions, such as temperature, pressure or environmental, in which it would not be practical to package other types of devices. More than likely, the variation from what is typically considered ambient conditions of room temperature and room pressure would occur with regard to the pressure.

35 For example, the packaging process may be accomplished with a starting low pressure subsequently varied to a higher pressure during the sealing process, which may aid in the formation of a good seal between the substrate and the back plate. The pressure may start at

vacuum, reach an intermediate pressure and then a higher pressure; may start at a low pressure and go to a higher pressure and any combination in between. Alternatively, the pressure may start at a first pressure and then vary to a low pressure.

Alternatively, the packaging process may be accomplished in a vacuum, pressure between
5 a vacuum up to and including ambient pressure, or pressure higher than ambient pressure and also including a varied and controlled high or low pressure during the sealing process. There may be advantages to packaging the arrays in a completely dry environment, but it is not necessary. Similarly, the packaging environment may be of an inert gas at ambient conditions. The ability to
10 package the devices in ambient conditions allows for a much lower cost procedure and potential for versatility in equipment choice since the device can be transported through ambient conditions without effecting the operation of device.

Many variations on pressures used during packaging could be employed. For example, sealing the back plate could be accomplished in an environment having a initial vacuum, where environmental pressure increases from vacuum to a higher pressure as sealing occurs such that, as
15 pressure in the package increases due to reduced volume, increased environmental pressure aids in creating a good seal.

In another example, the back plate could be sealed in an environment having a initial pressure higher than vacuum up to an including ambient pressure, where environmental pressure then increases to a higher pressure than the initial pressure as sealing occurs such that, as pressure
20 in the package increases due to reduced volume, increased environmental pressure aids in creating a good seal.

In another example, the back plate could be sealed in an environment having an initial environmental pressure higher than ambient pressure, wherein the environmental pressure then increases to a higher pressure than the initial pressure as sealing occurs such that, as pressure in
25 the package increases due to reduced volume, increased environmental pressure aids in creating a good seal.

In the above examples, a partial adhesive curing process in combination with the variation on pressure could affect the shape of the package. For example, the increased pressure inside the package during sealing may create a convex backplane shape by partial adhesive curing during a
30 period of time in which the package is at the higher pressure.

Once the back plate and the substrate are joined, there may be a hardening of the seal. In order to differentiate between the seal between the back plate and the substrate, and seals that may be applied after division into individual arrays, the seal between the back plate and the substrate is referred to here as the back seal. This is the seal that may be hardened, if necessary.

35 Once the seal is hardened, if necessary, the substrate is scribed or otherwise prepared for division. The wafer may be mechanically scribed or laser scribed. The substrate is then broken into individual devices. This may be an actual breaking process, or may even be a sawing

process, among other examples. Depending upon the seal, as well as other factors, it may become necessary after the division to apply an end seal to the packages, to complete the package. The endseal can be referred to as an opening in an otherwise continuous perimeter seal or the use of a continuous perimeter seal in combination with a hole in the back plate. To apply an endseal to the package may involve sealing the hole in the perimeter seal or sealing the hole in the back plate, whichever is acting as the endseal port.

The interferometric modulator will generally be formed on the transparent substrate and the back plate will be recessed to accommodate the modulators and desiccant and allow the modulators to continue to function. For example, providing a back plate in Figure 3 may involve providing a back plate having recessed regions within which the interferometric modulator manufactured on the transparent substrate will fit. An example of this is shown in Figure 4. The back plate 30 has regions 56 that are recessed regions in which the modulators will be accommodated. The 'footprints' of the modulators on the backside of the transparent substrate 12 are shown by the shaded regions 55. The back plate 30 is sealed to the transparent substrate and the modulators are encased in the recessed regions 56.

It must be noted that recessed regions are not necessary, but may lead to a longer lifetime of the device. For applications in systems which have reduced lifetimes, or in which a reduce lifetime is desired, the back plate may not have recessed regions. The recessing could be accomplished by application of a thicker seal material, or an external fillet. However, since the seal material does not form a hermetic seal, the thicker seal may allow more water vapor or other contaminants to enter the package, reducing the lifetime of the device.

The recessed regions could be formed in many different ways. One example would be for the recessed regions by forming ribs on the back plate, such as by slurried glass. Another example would be to printing a seal material on the back plate in a pattern to form the ribs. The ribs could be deposited, or stenciled. The recessed regions could be formed in the back plate itself by eroding portions of the back plate, such as by sandblasting or etching. The back plate could be molded or sagged to form the recessed regions. The back plate in the configuration of Figure 5 can have recesses but it is not necessary as the intermediate frame creates the cavities.

In an alternative, shown in Figure 5, an intermediate frame 58 could be mounted on the substrate 12 or the back plate 30. and then the back plate applied. In this instance, the recessed regions are formed between the transparent substrate and the areas of the frame that do not contact the back plate.

In the alternative, the recessed regions could be formed as a function of the transparent substrate, rather than the back plate. For example, the transparent substrate could have integrated spacer posts or the integrated spacers could be on the back plate. The posts would be positioned to be between the multiple modulator arrays on the substrate. Another example would be to provide a back plate of a flexible, polymer sheet that can be flexed to allow the offset between the

back plate and the substrate. The above examples are only intended as examples, and are not intended to limit application of the invention in any way.

Returning to Figure 3, the process of applying desiccant may also have several variations. For example, the desiccant could be applied after the substrate and back plate were aligned and joined. A small hole could be left on the back of the back plate, allowing desiccant to be injected into the individual device packages. In this embodiment, sealing the back plate to the substrate also requires the application of a seal to the hole to complete the joining between the back plate and the substrate to form the package. In this case, the desiccant injection hole can also facilitate a continuous perimeter seal process eliminating the typical LCD-type endseal hole used in the perimeter seal. An embodiment of a substrate and back plate having these holes is shown in Figure 6. The holes 60 in the back plate 30 would be sealed after injection of the desiccant.

The end seal may or may not be necessary. For example, an adhesive may be used in the areas between the modulator regions that allow the substrate to be scribed and broken without causing a break in the seal that requires an end seal process. In an initial non-continuous seal process the adhesive may be a material that can 'flow' such that the hole is filled in by the existing adhesive. In this embodiment, the seal would be self-healing, where self-healing is a mechanism where the adhesive is allowed to flow to close the hole that was initially present as the back plate and substrate were joined. The adhesive or other material may move to fill the hole in ambient conditions or with heat application.

In an alternative to avoid the end seal process, the seal lines of joining the back plate to the transparent substrate may be formed to be extra wide. When the substrate is broken, the width of the seal line would prevent the seal from being broken. This may use up too much of the transparent substrate in unusable space, when compared to the cost and complexity of an end seal process. The costs and benefits may differ from process to process and the ultimate selection of which is more beneficial would be left to the process designer.

In this manner, the packaging of an interferometric MEMS modulator in ambient conditions is provided. As mentioned above, the adhesive or other joining material used to join the substrate to the back plate need not form a hermetic seal. A desiccant may be applied to handle any moisture from water vapor that enters the package. As can be seen in Figure 7, the desiccant 34 absorbs the moisture that travels through the joint between the adhesive and either the back plate or the substrate as well as through the adhesive itself.

For example, it is desirable to keep the package as thin as possible for viable portable display application while still allowing enough desiccants to support the lifetime expectation of the device. One test predicted a reasonable lifetime of over 10 years at 40 degrees Celsius and 90% relative humidity. The adhesive seal was between 10 and 30 microns of thickness, 2 millimeters in width and a water vapor permeability coefficient of 0.2 to 0.4 gr-mm/m²-kPa-day.

These conditions are far more rugged than typical operating environments, but were indicative of the success of the package.

Thus, although there has been described to this point a particular embodiment for a method and apparatus for packaging interferometric modulators, it is not intended that such
5 specific references be considered as limitations upon the scope of this invention except in-so-far as set forth in the following claims.

WHAT IS CLAIMED IS:

1. A package, comprising:
a transparent substrate;
an interferometric modulator on the substrate;
a back plate;
a seal joining the back plate to the substrate to form a package; and
a desiccant inside the package.
2. The package of claim 1, wherein the transparent substrate comprises at least one of: glass, plastic, and transparent polymers.
3. The package of claim 1, wherein the seal comprises a non-hermetic seal.
4. The package of claim 1, wherein the seal comprises a hermetic seal.
5. The package of claim 1, wherein the interferometric modulator comprises at least one of: a membrane modulator, and a separable modulator.
6. The package of claim 1, wherein the back plate comprises at least one of: glass, metal, foil, polymer, plastic, ceramic and semiconductor materials.
7. The device of claim 1, wherein the seal comprises at least one of: epoxy-based adhesives, o-rings, PIBs, poly-urethane, thin film metal welds, liquid spin-on glass, solder, polymers, and plastics.
8. The device of claim 1, wherein the desiccant comprises at least one of: zeolites, molecular sieves, desiccants, surface adsorbents, bulk adsorbents, and chemical reactants.
9. A method of packaging an interferometric modulator, comprising:
providing a transparent substrate;
manufacturing an interferometric modulator array on a side of the substrate;
providing a back plate;
applying a desiccant to the back plate;
sealing the back plate to the side of the substrate with a seal in ambient conditions.
10. The method of claim 9, the method further comprising:
sealing in non-ambient conditions as a controlled environment of one of either inert gas or dry air, instead of sealing the back plate in ambient conditions.
11. The method of claim 9, the method further comprising:
hardening the seal;
scribing the substrate between multiple packages;
breaking the substrate into individual devices; and
applying an end seal to the individual devices.
12. The method of claim 9, wherein providing a back plate comprises providing a back plate having recessed regions.

13. The method of claim 12, wherein providing a back plate with recessed regions comprises forming ribs on the back plate.
14. The method of claim 13, wherein forming ribs on the back plate comprises patterning ribs onto the back plate.
15. The method claim 13, wherein forming ribs on the back plate comprises printing a material onto the back plate.
16. The method of claim 13, wherein forming ribs on the back plate comprises stenciling a material onto the back plate.
17. The method of claim 12, wherein providing a back plate having recessed regions comprises eroding the back plate in those regions to form cavities.
18. The method of claim 12, wherein providing a back plate having recessed regions comprises providing a back plate having molded regions.
19. The method of claim 12, wherein providing a back plate having recessed regions comprises mounting a frame on a flat back plate, the frame defining cavities between struts of the frame and the transparent substrate.
20. The method of claim 9, wherein providing a back plate comprises providing a sheet of foil with integral spacer posts such that the spacer posts lie between multiple interferometric modulator arrays on the transparent substrate.
21. The method of claim 9, wherein providing a transparent substrate comprises providing a transparent substrate with recessed regions in which the interferometric modulators are formed.
22. The method of claim 9, wherein providing a back plate comprises providing a flexible, polymer sheet.
23. The method of claim 9, wherein providing a transparent substrate further comprising providing a transparent substrate having integrated spacers.
24. The method of claim 9, wherein applying the desiccant comprises:
 - aligning the transparent substrate to the back plate;
 - joining the transparent substrate to the back plate;
 - sealing the back plate to the transparent substrate.
25. The method of claim 9, wherein applying the desiccant comprises:
 - applying the desiccant through a hole; and
 - sealing the hole.
26. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate with a self-healing seal.
27. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing a hole in the back plate.

28. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises forming a seal line of a width that is sufficient to allow scribing and breaking of the substrate without breaking the seal.

29. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in a vacuum environment.

30. The method of claim 29, further comprising maintaining the vacuum pressure in the package.

31. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in an environment having an initial vacuum, and wherein environmental pressure increases from vacuum to a higher pressure as sealing occurs such that, as pressure in the package increases due to reduction in volume, increased environmental pressure aids in improving the seal.

32. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in an environment having a pressure at higher than vacuum up to and including ambient pressure to form the seal.

33. The method of claim 31, further comprising maintaining the higher pressure at ambient pressure in the package.

34. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in an environment having an initial pressure higher than vacuum up to and including ambient pressure, and wherein environmental pressure increases to a pressure higher than the initial pressure as sealing occurs such that, as pressure in the package increases due to reduction in volume, increased environmental pressure aids in improving the seal.

35. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in an environment having a pressure higher than ambient pressure to form the seal.

36. The method of claim 35, further comprising maintaining the pressure in the package at a level that is higher than ambient pressure.

37. The method of claim 9, wherein sealing the back plate to the transparent substrate comprises sealing the back plate to the transparent substrate in an environment having an initial environmental pressure higher than ambient pressure, and wherein the environmental pressure increases to a pressure higher than the initial pressure as sealing occurs such that, as pressure in the package increases due to reduction in volume, increased environmental pressure aids in improving the seal.

38. The method of claim 31, wherein the increased pressure inside the package during sealing creates a convex backplane shape by partial adhesive curing during a period of time in which the package is at a level that is higher than the initial pressure.

39. The method of claim 34, wherein the increased pressure inside the cavity during sealing creates a convex backplane shape by partial adhesive curing during a period of time in which the cavity is at said higher pressure level.

40. The method of claim 37, wherein the increased pressure inside the package during sealing creates a convex backplane shape by partial adhesive curing during a period of time in which the package is at said higher pressure level.

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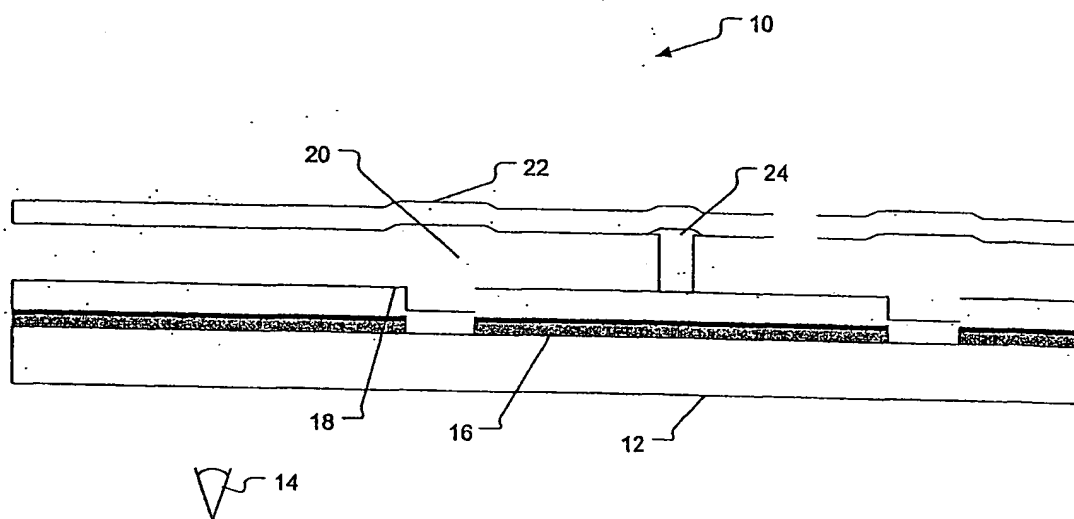


Figure 1a

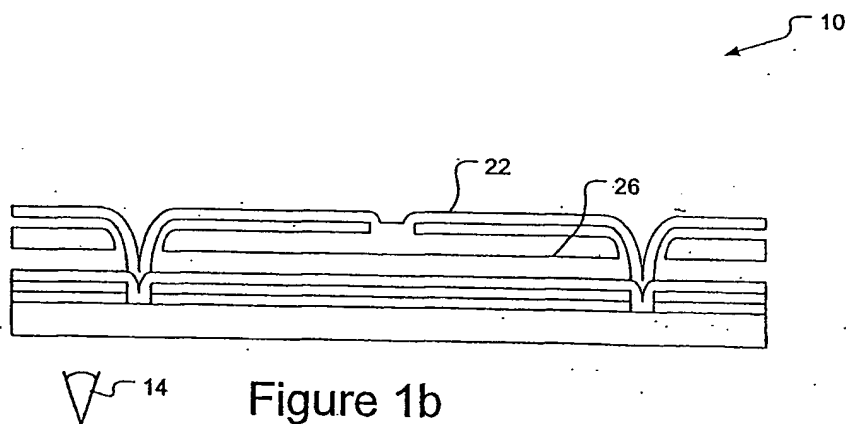
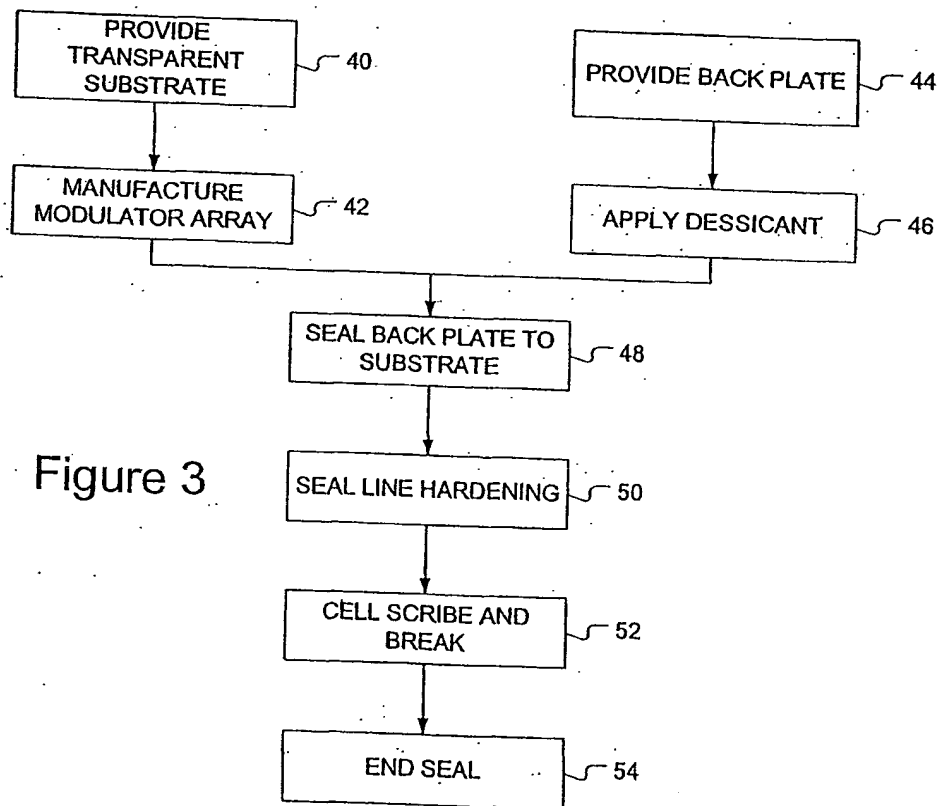
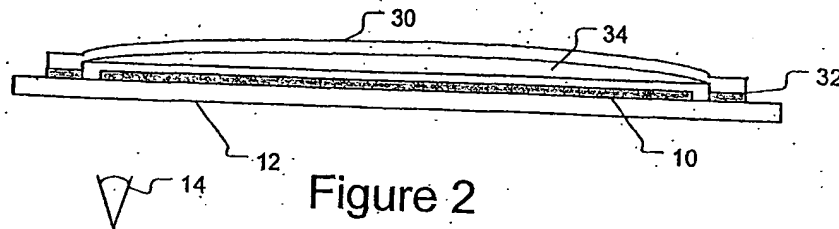


Figure 1b

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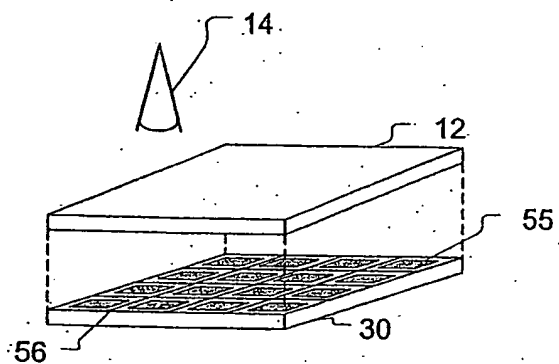


Figure 4

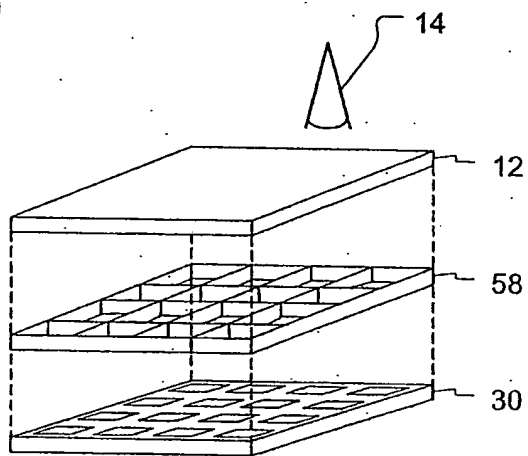
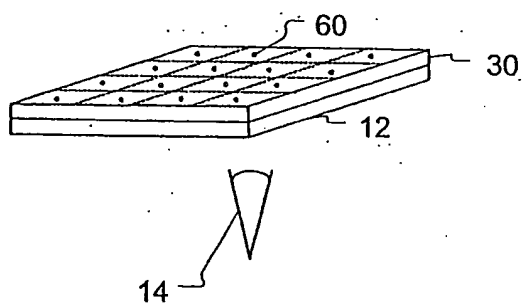


Figure 5

Figure 6



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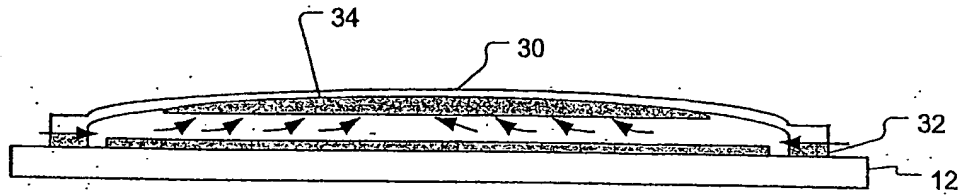


Figure 7

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US2005/013464

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 G02B26/00

According to international Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G02B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EP0-Internal, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2002/024711 A1 (MILES MARK W) 28 February 2002 (2002-02-28) paragraphs [0147], [0154], [0172], [0181]; figure 19 -----	1-19, 23-28
Y	US 2002/056900 A1 (LIU JWEI WIEN ET AL) 16 May 2002 (2002-05-16) paragraphs [0006], [0028], [0029]; claims 9,10,15,16; figures 1,5 -----	1-19, 23-28
A	US 5 939 785 A (KLONIS ET AL) 17 August 1999 (1999-08-17) column 4, line 34 - column 6, line 13; claim 2; figures 1,2 -----	1,9
A	US 2002/054424 A1 (MILES MARK W) 9 May 2002 (2002-05-09) paragraphs [0073] - [0076]; figures 5a,5b ----- -/-	1,9,21

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Date of the actual completion of the international search

25 July 2005

Date of mailing of the international search report

24 10. 2005

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INTERNATIONAL SEARCH REPORT

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PCT/US2005/013464

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>US 2004/051929 A1 (SAMPSELL JEFFREY BRIAN ET AL) 18 March 2004 (2004-03-18) paragraph [0029] - paragraph [0054]; figures 3,12a,12b -----</p>	<p>1,9,20, 22</p>

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2005/013464

Box II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claims Nos.:
because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically:
3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. ☐ As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims.
2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.
3. ☐ As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.:
4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-28

Remark on Protest

- ☐ The additional search fees were accompanied by the applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-28

The package having a transparent substrate provided with recessed regions in which the interferometric modulators are formed.

2. claims: 29-40

The package wherein the back plate is sealed to the transparent substrate in an environment having a pressure at higher than vacuum up to and including ambient pressure to form the seal.

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/US2005/013464

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2002024711 A1	28-02-2002	US 6674562 B1 US 2002075555 A1	06-01-2004 20-06-2002
US 2002056900 A1	16-05-2002	NONE	
US 5939785 A	17-08-1999	NONE	
US 2002054424 A1	09-05-2002	US 2003043157 A1	06-03-2003
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